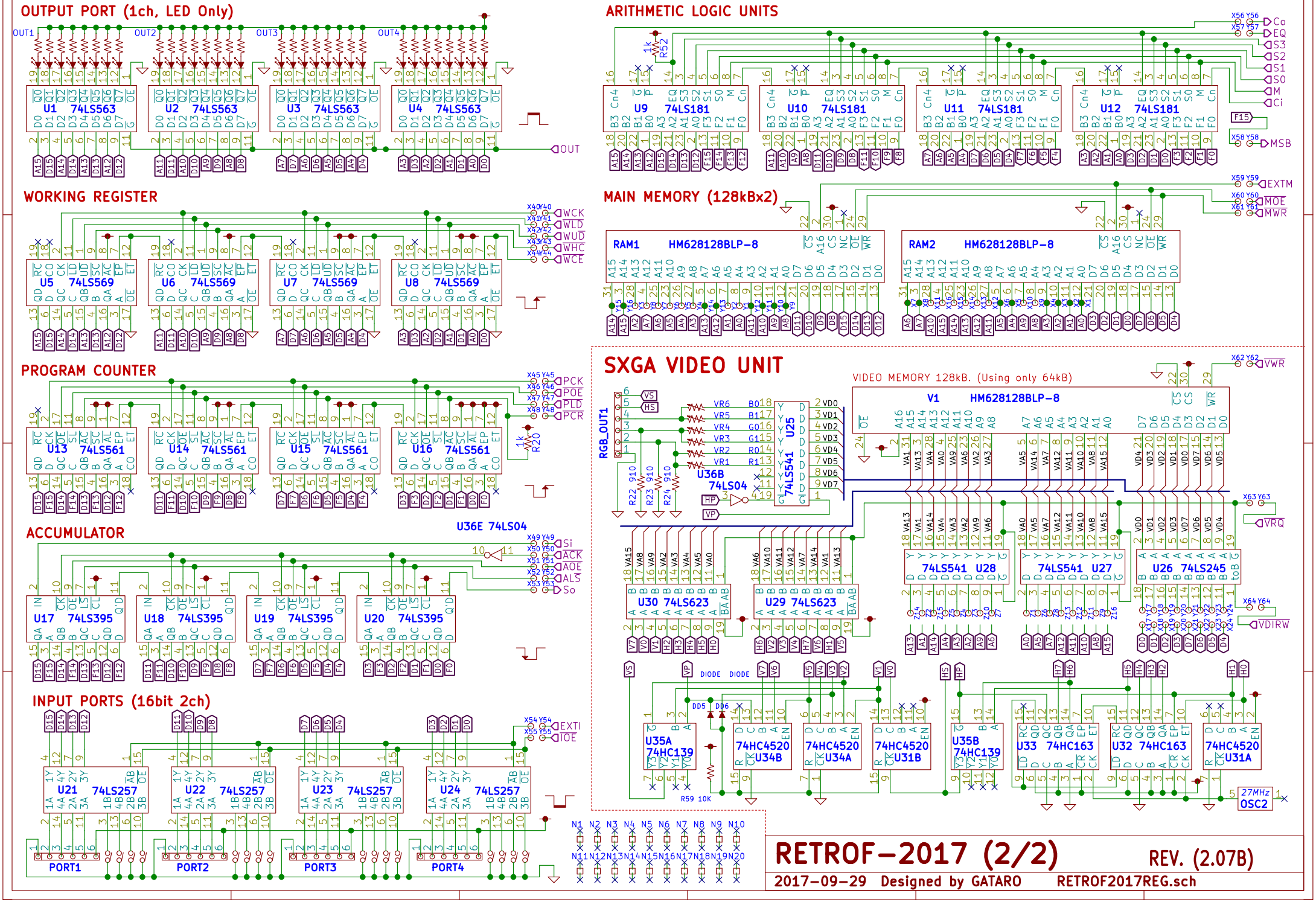
OUTPUT PORT (1ch, LED Only)
OUT1
OUT2
OUT3
OUT4
U1 74LS563
U2 74LS563
U3 74LS563
U4 74LS563
OUT
WORKING REGISTER
U5 74LS569
U6 74LS569
U7 74LS569
U8 74LS569
WCK
WLD
WUD
WHC
WCE
PROGRAM COUNTER
U13 74LS561
U14 74LS561
U15 74LS561
U16 74LS561
PCK
POE
PLD
PCR
ACCUMULATOR
U36E 74LS04
U17 74LS395
U18 74LS395
U19 74LS395
U20 74LS395
Si
ACK
AOE
ALS
So
INPUT PORTS (16bit 2ch)
U21 74LS257
U22 74LS257
U23 74LS257
U24 74LS257
EXTI
IOE
PORT1
PORT2
PORT3
PORT4
ARITHMETIC LOGIC UNITS
U9 74LS181
U10 74LS181
U11 74LS181
U12 74LS181
Co
EQ
S3
S2
S1
S0
M
Ci
MSB
MAIN MEMORY (128kBx2)
RAM1 HM628128BLP-8
RAM2 HM628128BLP-8
EXTM
MOE
MWR
SXGA VIDEO UNIT
VIDEO MEMORY 128kB. (Using only 64kB)
V1 HM628128BLP-8
VWR
VRQ
VDIRW
RGB_OUT1
U25 74LS541
U36B 74LS04
74LS541 U28
74LS541 U27
U26 74LS245
U30 74LS623
U29 74LS623
DIODE
U35A 74HC139
U35B 74HC139
74HC4520 U34B
74HC4520 U34A
74HC4520 U31B
74HC4520 U31A
U33 74HC163
U32 74HC163
27MHz OSC2
RETROF-2017 (2/2)
REV. (2.07B)
2017-09-29 Designed by GATARO
RETROF2017REG.sch