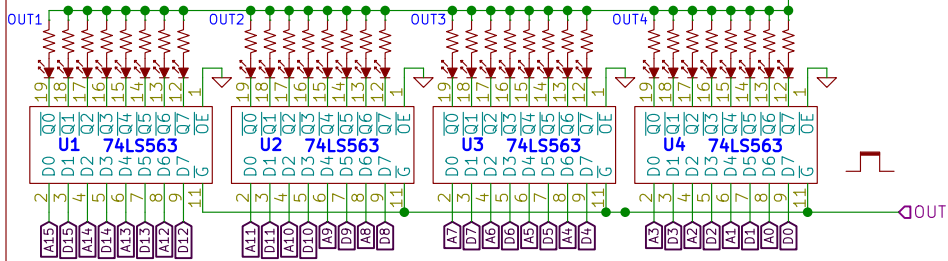
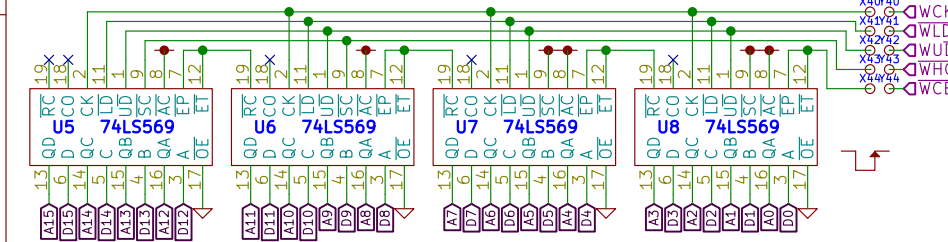


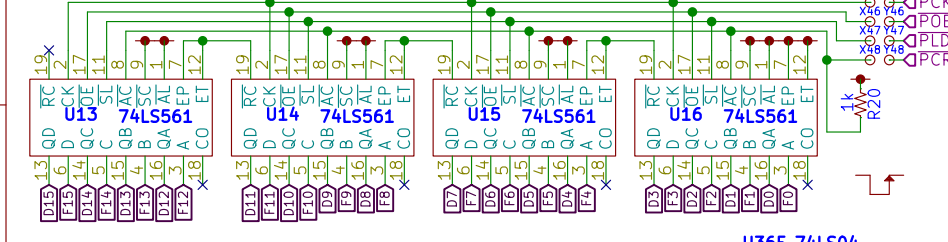
OUTPUT PORT (1ch, LED Only)



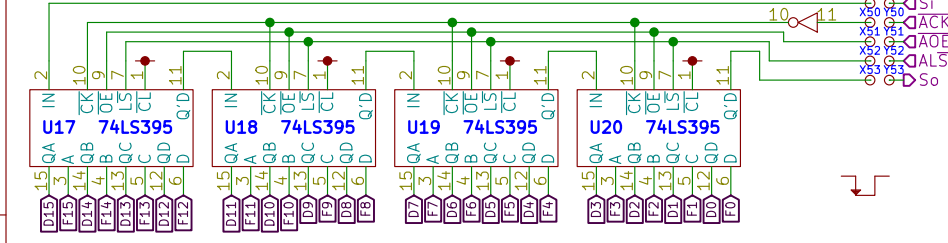
WORKING REGISTER



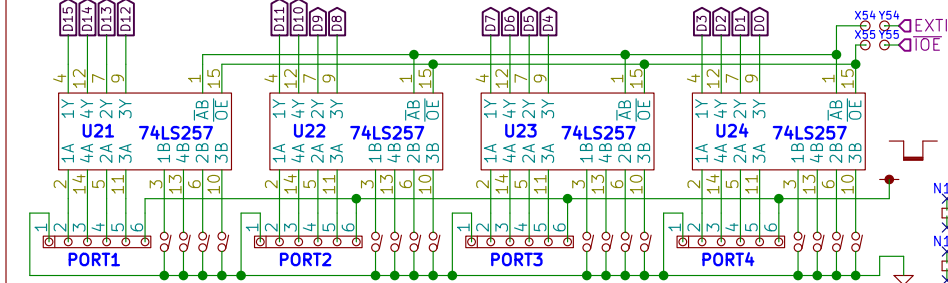
PROGRAM COUNTER



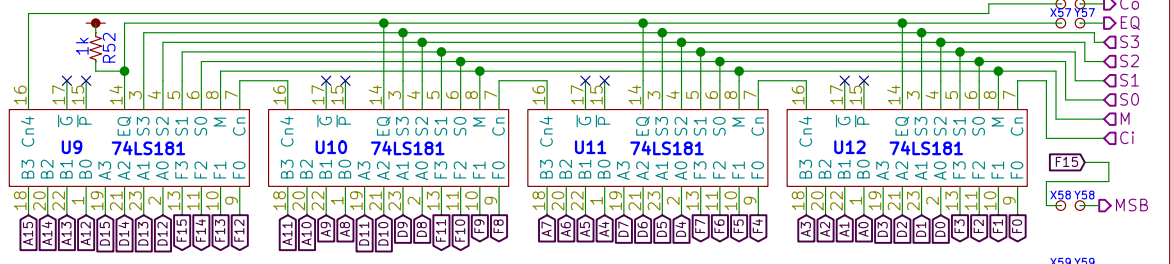
ACCUMULATOR



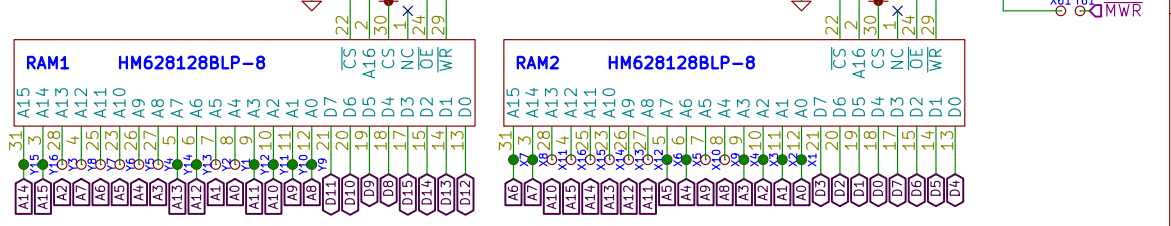
INPUT PORTS (16bit 2ch)



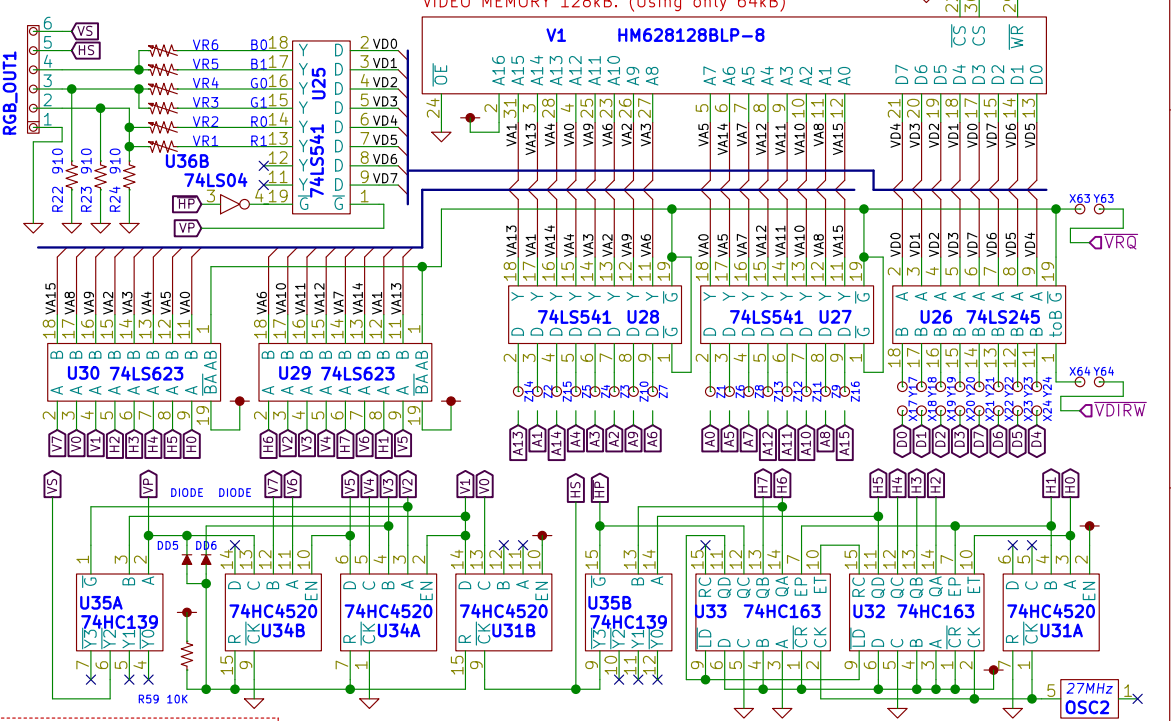
ARITHMETIC LOGIC UNITS



MAIN MEMORY (128kBx2)



SXGA VIDEO UNIT



RETROF-2017 (2/2)

REV. (2.06A)

2017-09-18 Designed by GATARO

RETROF2017REG.sch

X-E1 N1
 X-E2 N2
 X-E3 N3
 X-E4 N4
 X-E5 N5
 X-E6 N6
 X-E7 N7
 X-E8 N8
 X-E9 N9
 X-E10 N10
 X-E11 N11
 X-E12 N12
 X-E13 N13
 X-E14 N14
 X-E15 N15
 X-E16 N16
 X-E17 N17
 X-E18 N18
 X-E19 N19
 X-E20 N20